

31046 U.S. PTO

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01/22/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10054441	01 22 2002	250		2811	
**APPLICANTS: Back Josef					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CON OF PCT DE00 02293 07 13 2000					
** FOREIGN APPLICATIONS VERIFIED: GERMANY 199 33 959 7 07/20/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
To USO 110 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				Z&PINFN10455	
Verified and Acknowledged Examiners initials					
TITLE : Process for producing two differently doped adjacent regions in an integrated semiconductor					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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